Customer No.: 31561
Application No.: 10/604,248
Docket No.: 9174-US-PA

AMENDMENTS

In The Claims

1. (currently amended) A noise suppressing method for switching on a flat panel display driven by a time controller and a plurality of driver IC's, the noise suppressing method comprising:

providing a signal detect circuit and a video signal processor; and detecting whether a signal input to the flat panel display is stable by the signal detect circuit, and when the signal is unstable, controlling the driver IC's to output a black burst signal by the video signal processor.

- 2. (original) The noise suppressing method according to claim 1, further comprising embedding the signal detect signal in the time controller IC.
- 3. (original) The noise suppressing method according to claim 1, further comprising embedding the video signal processor in the time controller IC.
- 4. (original) The noise suppressing method according to claim 1, wherein the video signal processor controls the driver IC's to output a normal display signal when the signal detected by the signal detect circuit is stable.

Claims 5-7. (canceled).

8. (currently amended and withdrawn) The noise suppressing method according to claim 1, further comprising: A noise suppression method for switching on/off a flat panel display-which is driven by a time controller IC and a plurality of driver IC's, the suppressing method comprising:

providing a signal detecting circuit and a video signal processor, wherein the

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signal detect-circuit detects whether a signal-input to the flat-panel display-is stable and a switch-off signal;

controlling-the driver IC's to output a black burst signal by the video-signal processor when the signal detected by the signal detect circuit is unstable while switching on the flat panel display; and

controlling the driver IC's to output a charge reset signal by the video signal processor when the a switch-off signal is detected by the signal detect circuit while switching off the flat panel display, and switching off the flat panel display after charge reset operation is performed.

- 9. (withdrawn) The noise suppressing method according to claim 8, further comprising embedding the signal detect signal in the time controller IC.
- 10. (withdrawn) The noise suppressing method according to claim 8, further comprising embedding the video signal processor in the time controller IC.
- 11. (withdrawn) The noise suppressing method according to claim 8, further comprising controlling the driver IC's to output a normal display signal by the video signal processor when the signal detected by the signal detect circuit is stable.